

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Golshan, Khosrow
Title: METHOD AND APPARATUS FOR OPTICAL
PROCESSING
Appl. No.: 09/630,883
Filing Date: 08/02/2000
Examiner: Chang, Audrey Y.
Art Unit: 2872
Confirmation No.: 7954

Mail Stop **ISSUE FEE**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

ISSUE FEE
AND
AMENDMENT UNDER 37 C.F.R. § 1.312 TRANSMITTAL

Enclosed herewith please find Issue Fee Transmittal Form PTOL-85(B) and Amendment under 37 C.F.R. § 1.312.

Fees in the amount of **\$1,510.00** for payment of the Issue Fee are being paid by credit card via EFS-Web.

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

The Examiner stated the following:

Of the prior art references considered, none has disclose *an optical logic circuit performing an inverse function* for optical signals generated by a *first and second semiconductor light sources*, wherein the logic circuit is comprised of a *semiconductor substrate, an optical layer overlaying the semiconductor substrate having a second semiconductor material* configured to provide a *plurality of optical pathways* forming the optical logic gate. The optical logic gate having a *first and second optical inputs coupled* to the first and second semiconductor light sources, an *interference region* coupled to the first and second optical input and an *optical output* coupled to the interference region. The interference region comprises the second semiconductor material. The interference region contains *no potential barriers partitioning* the interference region. The output light signal from the optical logic circuit is a Boolean logic output signal based on the input light signal.

REMARKS

These Comments on Statement of Reasons for Allowance under 37 C.F.R. § 1.104 are intended to be responsive to the Notice of Allowance dated December 18, 2008.

While the Applicant agrees that the allowed claims recite a combination of subject matter that is patentable over the cited references, the Applicant does not necessarily agree with or acquiesce in the reasoning of the Examiner. Moreover, the Applicant notes that various other subject matter and/or combinations of subject matter may be patentable for other reasons than those given by the Examiner. The Applicant expressly reserves the right to set forth additional and/or alternative reasons for patentability and/or allowance in connection with the present Application or any future proceeding.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by the credit card payment instructions in EFS-Web being incorrect or absent, resulting in a rejected or incorrect credit card transaction, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

Date 04/30/2009

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